

This is a full simulation of the microprocessor described in the documentation. In order to use these files, you must download and install the free simulator from:

<http://sourceforge.net/projects/cedarlogic/?source=dlp>

Once you load the file Micro2.cdl into the simulator you can double click on each of the ROM modules (Module0, Module1, Module2, and Module3) in turn. A dialog box appears allowing you to load the corresponding file into the design.

The program is loaded into RAM. Double click on the RAM block and enter the program code.