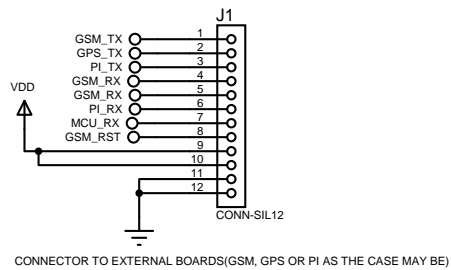
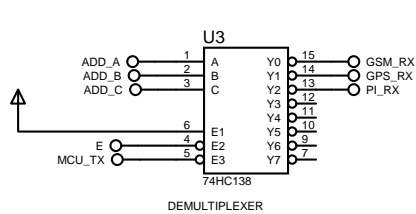


OUTPUT FROM PIC DEVICES(RX PIN) SENT TO ASSESSED DEVICE (A, B and C are address pins)



OUTPUT FROM SEVERAL DEVICES(TX PINS) ARE MULTIPLEXED INTO PIC RX PIN

